Lead MRAM System Test Engineer

Everspin Technologies has an immediate opening for a highly motivated, success-oriented, experienced DDR Test Engineer working in the Systems Test Group.

Scope of Responsibility/Expectations

The MRAM System Test Engineer will lead a small team, which owns the system level testing of DDR MRAM and associated products. This person will create and maintain verification test bench components and environments, need to be proficient with scripting and Verilog, as well as be able to come up with test plans that align with customer use cases.

Desired Skills and Experience

- Experience in post-silicon verification testing.
- Experience with Verilog.
- Knowledge of JEDEC DDR specifications.
- Experience with one or more scripting languages (Perl, Python, Ruby, shell). Python preferred.
- Experience working under strict schedule deadlines with the ability to manage multiple priorities.
- Excellent communication skills and ability to collaborate with design engineering, systems engineering, and applications engineering.
- Team lead and management experience is a plus

Education and Experience

BS in electrical engineering, computer engineering, or equivalent education and experience, is required.

The Company

Everspin Technologies is the world's first volume MRAM supplier and continues to lead the industry by advancing the technology and expanding its MRAM product portfolio. The open position is at the Austin Design Center and not at the company headquarters located in Chandler, Arizona (greater Phoenix metropolitan area). It is a salaried position that includes benefits including: medical, dental, vision plan, paid time off, and a 401k retirement plan.

Contact

Please send resume to jobs@everspin.com